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Class	Subclass
<b>ISSUE CLASSIFICATION</b>	

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PATENT NUMBER

U.S. **UTILITY** Patent Application

**O.J.P.E.**

**PATENT DATE**

SCANNED

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APPLICATION NO. 09/942328	CONT/PRIOR	CLASS <del>365</del> 711	SUBCLASS 141	ART UNIT <del>2818</del> 2186	EXAMINER M. Kim H. Kim
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## APPLICANTS

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### 37411

Streamlined cache coherency protocol system and method for a multiple processor single chip device

[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner) (Date)			<b>ISSUE FEE</b>	
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